Book Publisher International



F. No. SDI/BP/7685D/3860 Dated 25Th November 2020

То

G Prasanna Kumar¹, J.Prabhakar², Nagulancha Raju³

1Assistant prof, ECE Dept. Malla Reddy Engineering College(A),Hyderabad,Telangana,India.
²Assistant prof, ECE Dept. Nalla Malla Reddy Engineering College, Hyderabad, India.
3Assistant prof, ECE Dept. Vignan Institute of Technology and Science Hyderabad,Telangana,India.

*Corresponding Author

Subject: Acceptance letter for manuscript (2020/BP/7685D) as a book chapter of Advanced Aspects of Engineering Research

Dear Dr. G Prasanna Kumar,

We are pleased to inform that your manuscript (Ref. no: **2020/BP/7685D**) entitled **"Frequency Multiplier with Delay Locked Loop -Based Clock Generator for System on Chip Applications."** is ACCEPTED for publication **as a book chapter in the following book:** <u>Advanced Aspects of Engineering Research</u>

Thank you for submitting your manuscript in Advanced Aspects of Engineering Research

Thanking you.

man

Dr. M. Basu Chief Managing Editor Book Publisher International

Reg. Offices: India: Book Publisher International, Guest House Road, Street no - 1/6, Hooghly, West Bengal, India, Corp. Firm Registration Number: L77527, Tele: +91 8617752708